WEST Search History

Hide Items | Restore | Clear | Cancel

DATE: Tuesday, January 03, 2006

Hide?	Set Name	Query	Hit Count
		DB=PGPB,USPT,JPAB; PLUR=YES; OP=OR	
☐	L9	L8 and (plac$ or rout$)	8
☐	L8	(boolean same target same (vhdl or hdl or verilog))	9
☐	L7	L6 and (boolean same target same (vhdl or hdl or verilog))	2
☐	L6	L5 and (circuit same type)	75
☐	L5	L4 and (fpga or (gate adj array))	82
☐	L4	L3 and representation	131
☐	L3	L2 and (synthesi$ or simulat$ or edit$) and target	157
☐	L2	L1 and (plac$ or rout$)	268
☐	L1	(verilog or vhdl or hdl) same (boolean)	320

END OF SEARCH HISTORY

Hit List

Search Results - Record(s) 1 through 8 of 8 returned.

☐ 1. Document ID: US 20040030912 A1

Using default format because multiple data bases are involved.

L9: Entry 1 of 8   File: PGPB   Feb 12, 2004

```
PGPUB-DOCUMENT-NUMBER: 20040030912
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040030912 A1
```

TITLE: Systems and methods for the prevention of unauthorized use and manipulation of digital content

PUBLICATION-DATE: February 12, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Merkle, James A. JR.	Rochester Hills	MI	US
LeVine, Richard B.	Marstons Mills	MA	US
Lee, Andrew R.	Marlborough	MA	US
Howard, Daniel G.	Mashpee	MA	US
Goldman, Daniel M.	North Falmouth	MA	US
Pagliarulo, Jeff	West Barnstable	MA	US
Hart, John J. III	Mashpee	MA	US
Bouza, Jose L.	Osterville	MA	US

US-CL-CURRENT: 726/26

Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw D

☐ 2. Document ID: US 20020100001 A1

L9: Entry 2 of 8   File: PGPB   Jul 25, 2002

```
PGPUB-DOCUMENT-NUMBER: 20020100001
PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20020100001 A1
```

TITLE: Active trace debugging for hardware description languages

PUBLICATION-DATE: July 25, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Lai, Ming-Chih	Hsin-Chu		TW

Lee, Chia-Huei	Hsin-Chu	TW
Ho, Bang-Hwa	Hsin-Chu	TW
Tsai, Jien-Shen	Hsin-Chu	TW

US-CL-CURRENT: 716/1

| Full | Title | Citation | Front | Review | Classification | Date | Reference | Sequences | Attachments | Claims | KWIC | Draw De |

☐ 3.   Document ID: US 6816825 B1

L9: Entry 3 of 8   File: USPT   Nov 9, 2004

US-PAT-NO: 6816825
DOCUMENT-IDENTIFIER: US 6816825 B1

TITLE: Simulation vector generation from HDL descriptions for observability-enhanced statement coverage

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 4.   Document ID: US 6587590 B1

L9: Entry 4 of 8   File: USPT   Jul 1, 2003

US-PAT-NO: 6587590
DOCUMENT-IDENTIFIER: US 6587590 B1

TITLE: Method and system for computing 8.times.8 DCT/IDCT and a VLSI implementation

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 5.   Document ID: US 6546526 B2

L9: Entry 5 of 8   File: USPT   Apr 8, 2003

US-PAT-NO: 6546526
DOCUMENT-IDENTIFIER: US 6546526 B2

TITLE: Active trace debugging for hardware description languages

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

☐ 6.   Document ID: US 6205574 B1

L9: Entry 6 of 8   File: USPT   Mar 20, 2001

US-PAT-NO: 6205574
DOCUMENT-IDENTIFIER: US 6205574 B1

TITLE: Method and system for generating a programming bitstream including identification bits

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | | Claims | KWIC | Draw D |

☐ 7. Document ID: US 5659484 A

L9: Entry 7 of 8   File: USPT   Aug 19, 1997

US-PAT-NO: 5659484
DOCUMENT-IDENTIFIER: US 5659484 A

TITLE: Frequency driven layout and method for field programmable gate arrays

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | | Claims | KWIC | Draw D |

☐ 8. Document ID: US 5648913 A

L9: Entry 8 of 8   File: USPT   Jul 15, 1997

US-PAT-NO: 5648913
DOCUMENT-IDENTIFIER: US 5648913 A
** See image for **Certificate of Correction** **

TITLE: Frequency driven layout system and method for field programmable gate arrays

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | | Claims | KWIC | Draw D |

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Terms	Documents
L8 and (plac$ or rout$)	8

Display Format: | - | Change Format |

Previous Page   Next Page   Go to Doc#